

## **BRIEF BIOGRAPHY OF CANDIDATE**

Kanika Monga received her B.Tech degree in Electronics and Communication Engineering from Guru Gobind Singh Indraprastha University, New Delhi, India, in 2014 and M.Tech degree in Electronics and Communication Engineering from Deenbandhu Chhotu Ram University of Science and Technology, Murthal, India in 2017. In her M.Tech research, she worked on design and implementation of low power static random access memory. She joined Birla Institute of Technology and Science, Pilani, India as a Research Scholar in 2017. Her research interests are VLSI Design, non-volatile memory and in-memory computing.

## **BRIEF BIOGRAPHY OF SUPERVISOR**

Dr. Nitin Chaturvedi received his Master of Science degree in Electronics from Devi Ahilya Vishwavidyalaya, Indore, in 2003, Master of Technology degree in Instrumentation from University Centre for Instrumentation and Microelectronics (UCIM), Panjab University, Chandigarh, in 2005, and Ph.D. degree from Birla Institute of Science & Technology, Pilani, in 2015. He joined Birla Institute of Technology & Science, Pilani in 2005 as an Assistant Lecturer and currently he is designated as Assistant Professor in the Department of Electrical & Electronics Engineering, Birla Institute of Technology and Science, Pilani. He has more than 15 years of experience in academia and has contributed several publications in National and International Journals in the area of microelectronics and VLSI design. His interest includes high performance cache architectures, CMOS VLSI design, asynchronous circuit design, non-volatile architectures for energy harvesting, in-memory computing, low-power neuromorphic circuit design and hardware security. He is currently working as a principle investigator on the National Super Computing Mission-DST sponsored project titled Design & Development of Hybrid Memories for High Performance Computing using Conventional CMOS Technology and Emerging Magnetic Technology.