

Brief Biography of candidate



Prateek Sikka is currently pursuing his PhD at BITS Pilani, Rajasthan, India. He is also working for NXP Semiconductors in Noida, Uttar Pradesh, India as a Principal Engineer. He has about 15 years of experience serving in multiple EDA and semiconductor organizations, such as STMicroelectronics, Cadence, and Mentor Graphics in both R&D and customer-facing roles. His research areas include High-Level Synthesis, Digital Systems' design and optimization, FPGA prototyping, and emulation. He holds seven patents, seven SCI journal publications, and multiple conference papers in the same field. He has conducted multiple FDPs, tutorials, and invited talks at multiple forums and institutes of repute in India. He holds a Bachelor of Engineering degree from Thapar Institute Patiala and a Master of Technology degree in Integrated Electronics from IIT, Delhi.

Brief biography of supervisor



Dr. Abhijit Asati completed Ph.D. degree (in 2010) and M.E. degree (in 2002) from BITS Pilani. Prior to this he completed B.E. degree in electrical engineering from Amravati university in the year 1996. He has more than 19 years of experience in academia and currently working as ‘Associate Professor’ in the ‘Electrical and Electronics Engineering Department’ at BITS Pilani. He also served as a faculty member at the Visveswarya National Institute of Technology, Nagpur from 1997 to 1999. He taught several courses such as Electrical Sciences, Microelectronic Circuits, Analog and digital VLSI design, CAD for IC Design, VLSI test and testability. Dr. Asati supervised 3 Ph. D. thesis and currently supervising 2 research scholars. He also supervised around 25 B.E. and M.E. thesis.

He contributed to more than 40 journal papers, more than 35 conference papers and one Indian patent published in the area of microelectronics, VLSI design and embedded system. His research contributions are in the area of high performance/low power VLSI design, microprocessor design, NBTI degradation issues and biometric identification. He visited "Cypress Semiconductor Limited, Bangalore", from May to July 2014 as a part of Industry Immersion Program of BITS, Pilani. He was also Visiting Assistant Professor "University of Virginia, Charlottesville", from May to July 2018 as a part of University Immersion Program of BITS, Pilani. He is senior member IEEE and member for IEEE Solid-State Circuits Society.

Brief biography of co-supervisor



Dr. Chandra Shekhar currently serves as a Senior Emeritus Professor at BITS, Pilani, Rajasthan, India. After attaining his Ph.D., he joined the Solid State Devices Division at CEERI, Pilani in 1977 as a scientist. Having worked for IC Design-related activities, he also served as the director at CEERI, Pilani. He was awarded the UNESCO/ROSTSCA Young Scientist Award in 1986, the CEERI Foundation Day Merit award in 1988, the ISHEER Science Councilor Award in 2005, the Prof. L. K. Maheshwari Foundation Distinguished Alumnus Award in 2010, a Doctor of Science (Honoris Causa) by NIT Kurukshetra in 2012 and the IETE Diamond Jubilee Gold Medal award in 2013.