

**BIRLA INSTITUTE OF TECHNOLOGY AND SCIENCE,  
PILANI**

**CERTIFICATE**

This is to certify that the thesis entitled “**Methodologies for Area, Speed, and Power Optimization in High-Level Synthesis for Diverse Applications**” submitted by **Prateek Sikka**, ID. No. **2017PHXF0501P** for award of Ph.D. of the Institute embodies original work done by him under my supervision.

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