

# **Parallel Multipliers: Architectural Exploration and Designing**

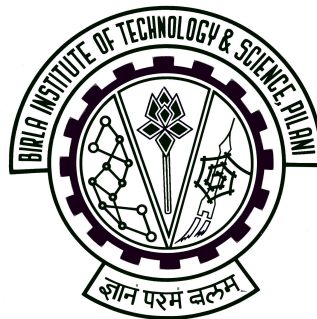
THESIS

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By

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